



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	:	10/727,595
Applicant	:	Gerhard Enders, et al.
Filed	:	December 5, 2003
TC/A.U.	:	Not Yet Assigned
Examiner	:	Unknown
Docket No.	:	0928.0006C
Customer No.	:	27896
Title	:	Integrated Semiconductor Memory With a Selection Transistor Formed At a Web

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to an examination on the merits, please amend the specification as follows:

Amendments to the Specification begin on page 2 of this paper.

Remarks begin on page 5 of this paper.